

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1.-21. (Canceled)

22. (Currently Amended) A semiconductor device comprising:

a first transistor;

a second transistor;

~~a plurality of inverters~~ a first inverter and a second inverter;

a first power source applying a first potential;

a second power source applying a second potential;

~~a first circuit configured to generate a third potential which is different from the first potential and the second potential; [[and]]~~

~~a second circuit configured to generate a fourth potential which is different from the first potential and the second potential; and~~

a first pixel including a first display element and a second pixel including a second display element,

wherein each of the ~~plurality of~~ first and second inverters comprising:

a third transistor; and

a fourth transistor,

wherein one of a source and a drain of the second transistor is electrically connected to the second power source,

wherein the other of the source and the drain of the second transistor is electrically connected to each of the ~~plurality of~~ first and second inverters,

wherein one of a source and a drain of the first transistor is electrically connected to the first power source,

wherein the other of the source and the drain of the first transistor is electrically connected to each of the ~~plurality of first and second~~ inverters,

wherein the first transistor is electrically connected to ~~[[and]]~~ the second transistor ~~are connected to each other with through~~ at least one of the ~~plurality of first and second~~ inverters ~~interposed therebetween~~,

wherein a gate of the first transistor is electrically connected to the second circuit,

wherein a gate of the second transistor is electrically connected to the first circuit,

wherein the first inverter is electrically connected to the first pixel, and

wherein the second inverter is electrically connected to the second pixel.

~~wherein a first signal is inputted to gates of the third transistor and the fourth transistor of each of the plurality of the inverters, and~~

~~wherein a second signal is outputted from one of a source and a drain of the third transistor and one of a source and a drain of the fourth transistor of each of the plurality of the inverters.~~

23.-24. (Canceled)

25. (Previously Presented) An electronic appliance using the semiconductor device according to claim 22.

26.-29. (Canceled)

30. (New) The semiconductor device according to claim 22, wherein the first circuit is configured to generate a third potential which is different from the first potential and the second potential, and the second circuit is configured to generate a fourth potential which is different from the first potential and the second potential.

31. (New) The semiconductor device according to claim 30,

wherein the first circuit includes multiple resistors connected in series,
wherein an end of the multiple resistors is electrically connected to the first power source,

wherein an end of another one of the multiple resistors is electrically connected to the second power source, and

wherein the third potential is outputted from a connecting node of two resistors that are selected from the multiple resistors.

32. (New) The semiconductor device according to claim 30,
wherein the second circuit includes multiple resistors connected in series,
wherein an end of the multiple resistors is electrically connected to the first power source,

wherein an end of another one of the multiple resistors is electrically connected to the second power source, and

wherein the fourth potential is outputted from a connecting node of two resistors that are selected from the multiple resistors.

33. (New) The semiconductor device according to claim 30,
wherein the third potential is different from the fourth potential.

34. (New) The semiconductor device according to claim 22,
wherein the first display element is a light-emitting element and the second display element is a light-emitting element.